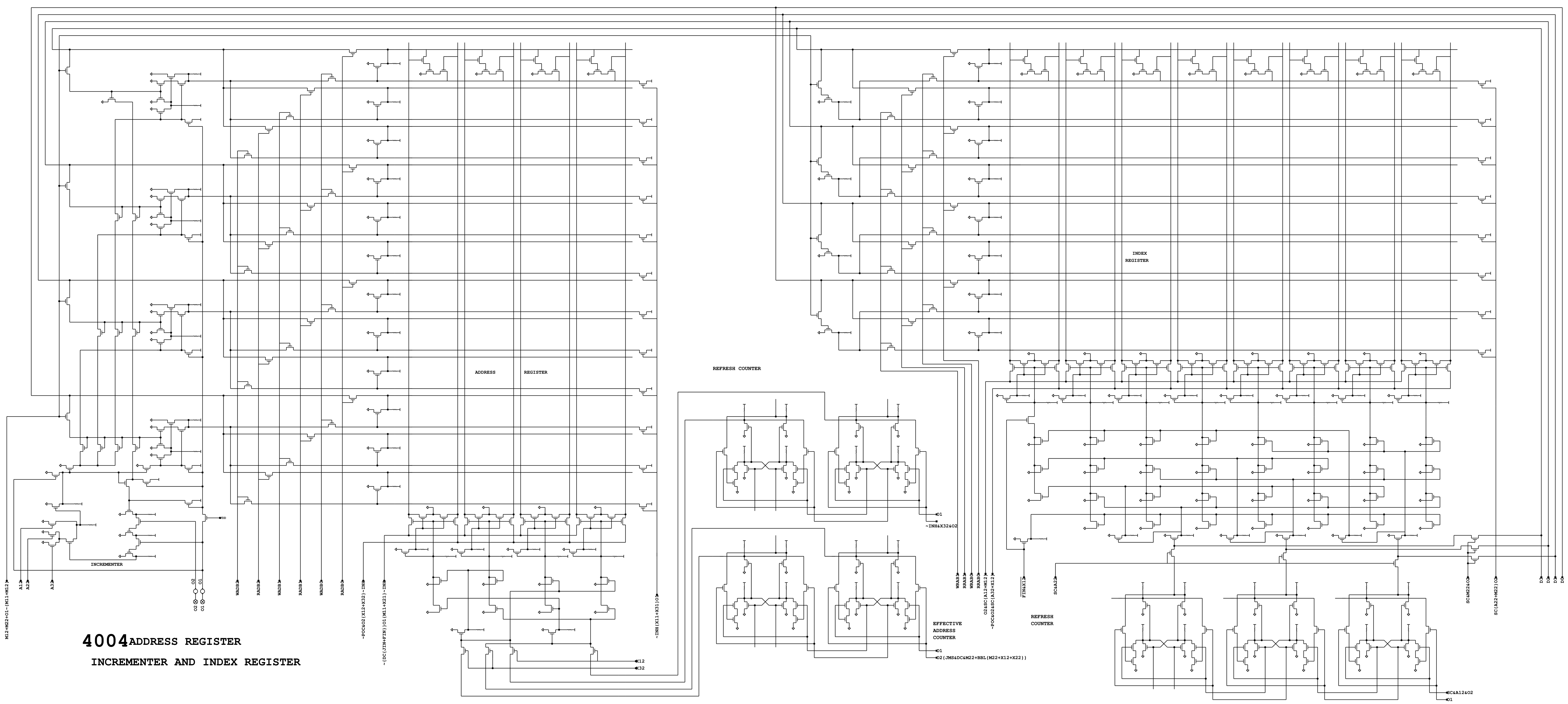
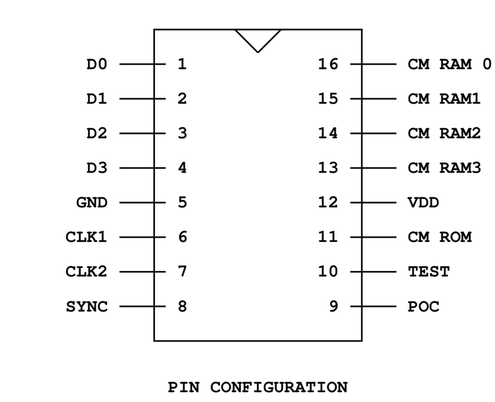
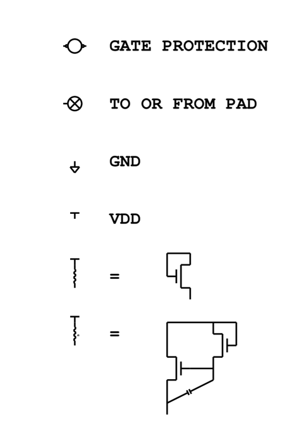
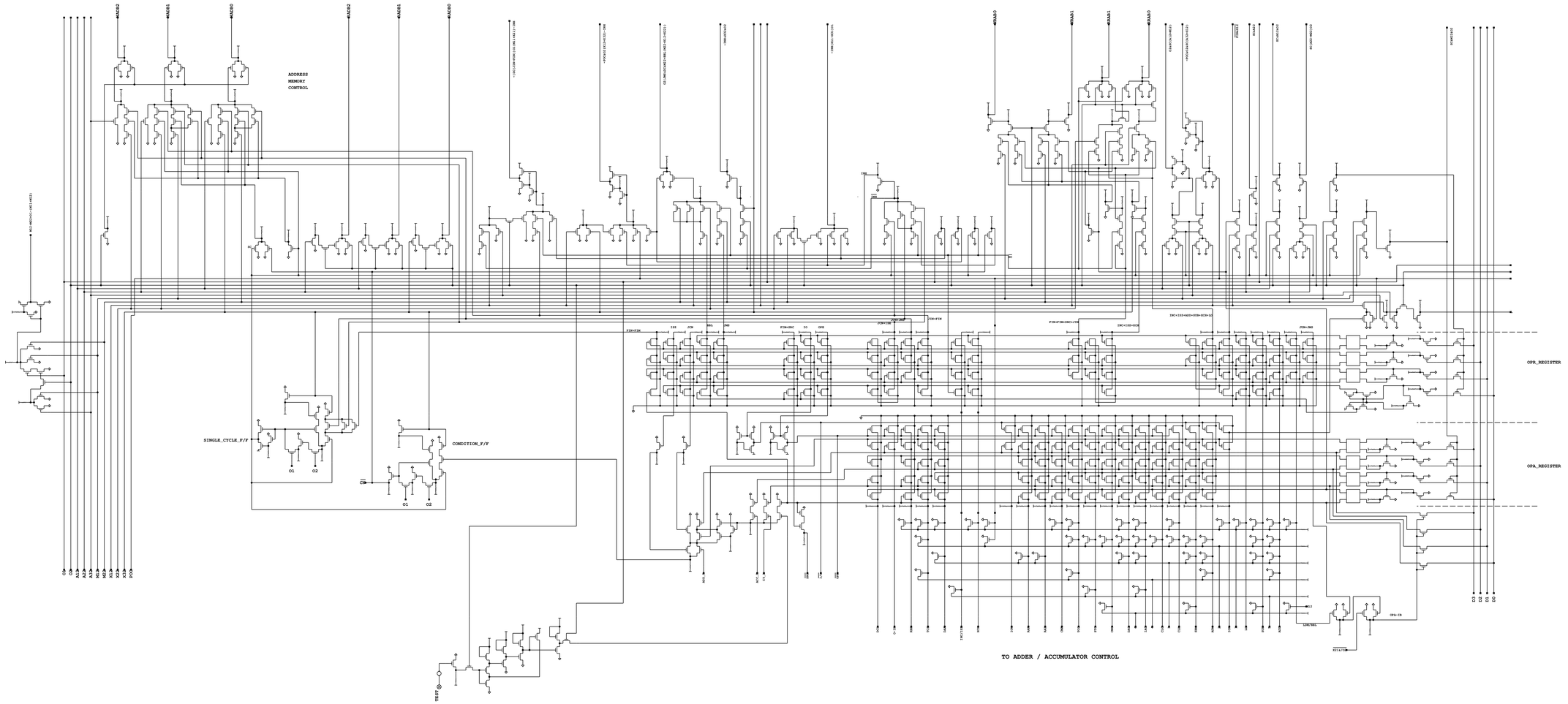


4004 ADDRESS REGISTER

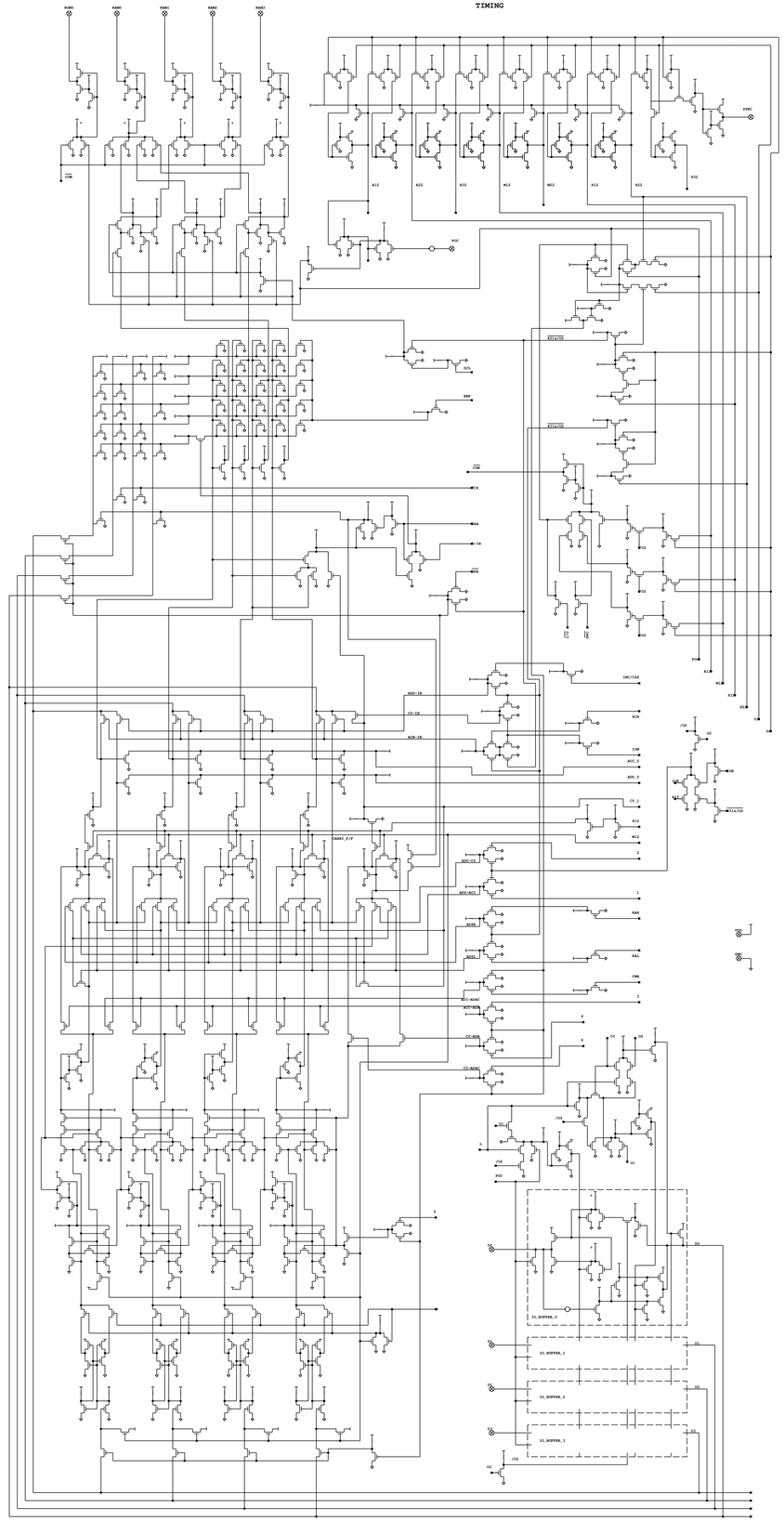
INCREMENTER AND INDEX REGISTER







TIMING



Re-drawn schematics based on Revision G of the original Intel 4004 schematics by Intel Corporation (August 6, 1976). Schematic capture and design verification by Fred Huettig, Brian Silverman and Barry Silverman (February 2, 2006).
